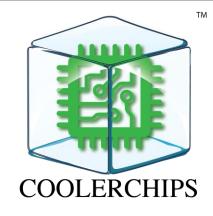


Aligned Graphite Microchannel Cooling (AGMC) System With Additively Manufactured Manifolds PI: Chris Roper, HRL Laboratories, LLC Team Members: Swiftech





Project Vision

- We are increasing the thermal-hydraulic performance of reliable, single-phase liquid cooling to meet the cooling demands of future data centers by *manufacturing* uncommon, highperformance *materials* in new cooling-block *architectures*.
- Rapid technology transition and commercial impact is enabled by focusing innovation on the cooling-blocks and leveraging existing COTS components for the rest of the data center cooling system.

Total Project Cost:	\$2.5M
Length	36 mo.

COOLERCHIPS Kickoff Meeting October 18 & 19, 2023

Fed. funding:	\$2.5M
Length	36 mo.

Team member	Location	Role in project, core competencies
HRL Laboratories, LLC	Malibu, CA	 Cooling block development and testing Oversight of chassis- and rack-level design, fabrication, and testing Expert in novel materials & manufacturing
Swiftech	Anaheim, CA	 Design, fabrication, and testing of chassis-level and rack-level demonstrations Reliability consulting and testing Liquid-cooling chassis- and rack-level packaging expert Heat transfer component manufacturer

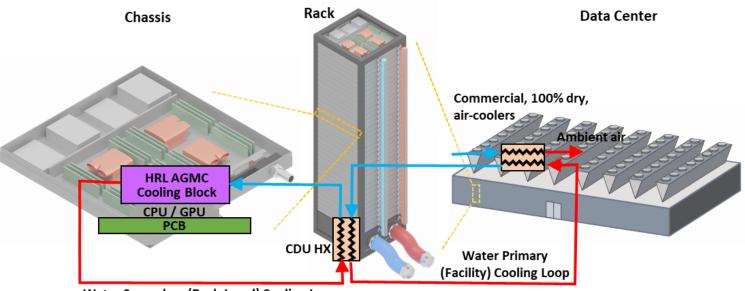
- Innovations in materials, manufacturing, and architectures for unique cooling-block
- COTS for rest of system and single-phase for reliability and tech. adoption



Concept Detail – System



- Single-phase water cooling solution integrated into proven COTS infrastructure
- System level performance metrics: thermal resistance below 0.0085 K/W at pumping powers <5% IT power



Water Secondary (Rack-Level) Cooling Loop

FOA Metrics	Units (Mid-Project)	Units (Final Project)			
Resistance Target	<0.015 K/W	<0.01 K/W			
Cooling Power % of IT_power		<3.0 % secondary loop <5.0 % total			
System availability	>99.982 %				
Chipset	Proxy chipset				
Chip Power	11	1 kW			
Power per server	4 kW/U (1U demonstration unit)	4 kW/U (4U demonstration unit)			
Demonstration power	4 kW	16 kW			



Concept Detail – Cooling Block

- Microchannel array with aligned graphite (with thermal conductivity 3-4X higher than traditional copper) for superior heat transfer from source to
- Additively manufactured flow manifold reduces channel flow length, thereby enabling acceptable pressure drop in microchannels
- 3. Tailored manifold and baseplate to match CTE of underlying chipset for increased reliability
- <u>Cooling block key performance metrics</u>: thermal resistance < 0.006 K/W at <1.5% IT load pumping power



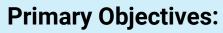
fluid

3





Task	#	Subtask
	1.1	Cooling Block Design
Design & Simulation	1.2	System Design
	1.3	Reliability Consulting
	2.1	Cooling Block Fabrication
Fabrication and Manufacturing	2.2	Chassis Fabrication
	2.3	Rack Fabrication
	3.1	Test Coord. & Analysis
Testing &	3.2	Cooling Block Test
Demonstration	3.3	Chassis Testing
	3.4	Rack Testing
Tech 2 Market	4.0	Strategy
Program Management	5.0	Program Management



- 1. Develop a world-record performance single-phase cooling block
- 2. Demonstrate system performance and reliability at chassis and rack levels
- 3. Position for maximum economic impact and transition



Challenges and Risks



- Technical and commercialization risks identified and evaluated
- Mitigation plans prepared for all risks

Risk Status

	Almost							Risk	#
Likelihood	Certain							Technology does not meet the FOA target for thermal resistance	а
	Likely			h				Technology does not meet the reliability target due to manifold attachment & manufacturing	b, c, d
	Moderate			fe	c a d		Technical	Technology does not meet the FOA target for cooling power as a percentage of IT load (i.e. Sufficiently low pressure drop and cooling power consumption)	е
	Unlikely				b	g		Technology does not meet the FOA target for compute power density in full rack	f
	Rare						Commer- cialization	Manufacturing cost prohibitively high.	g
								Differences from existing data center cooling systems lead to slow adoption rate	h
		Insignificant	Minor	Moderate	Major	Catastrophic			
		Consequences							



Needs and Potential Partnerships

Seeking to engage data center operators for input on unique data center requirements



ABORATORIES